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APPLICANTS

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**** CONTINUING DATA *******

This application is a DIV of 10/264,615 10/03/2002 PAT 6,875,646
 which is a DIV of 09/848,846 05/03/2001 *LT*
 which is a DIV of 09/388,856 09/01/1999 PAT 6,579,751

**** FOREIGN APPLICATIONS ******* *LT*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED
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Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY ID	SHEETS DRAWING 4	TOTAL CLAIMS 15	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after allowance				
Verified and Acknowledged	Examiner's Signature <i>Shreea</i>	Initials <i>LT</i>		

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TITLE
 Semiconductor processing methods of forming integrated circuitry

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RECEIVED 972		<input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue)

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